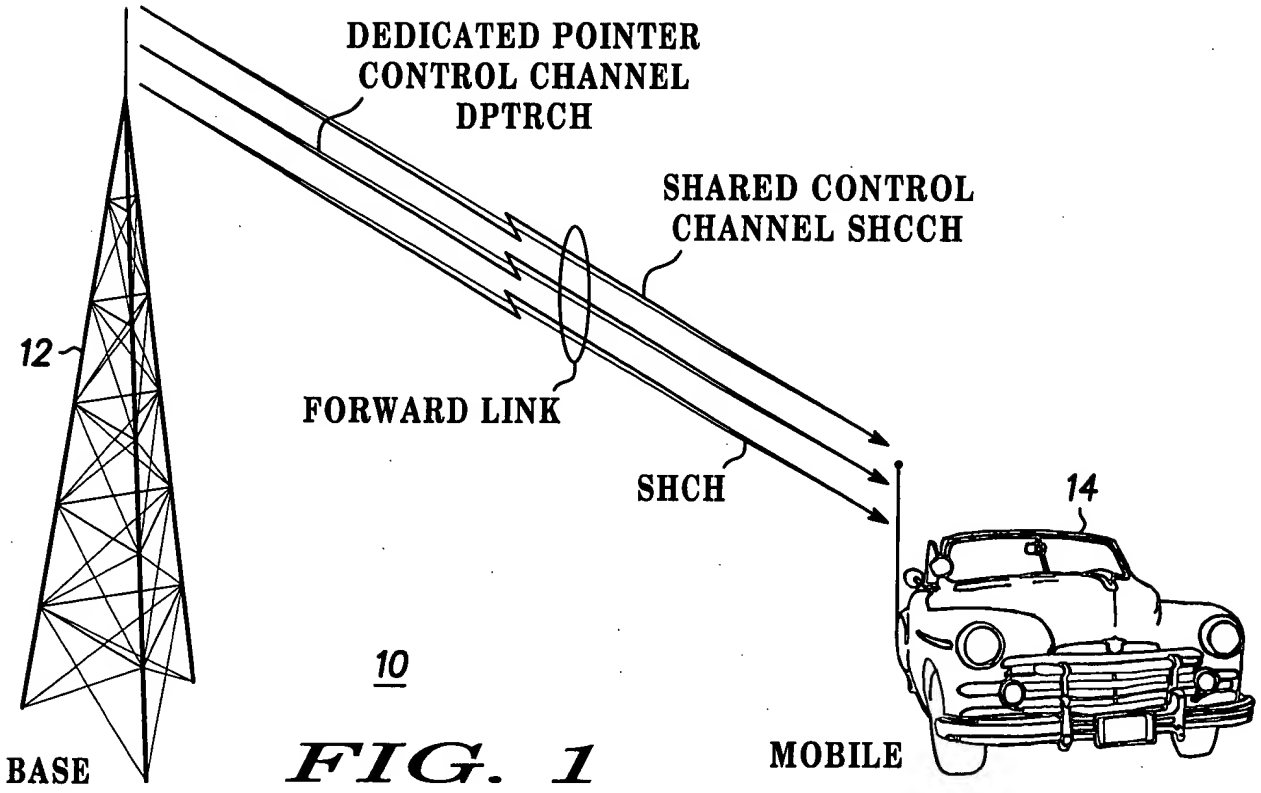

FIG. 1

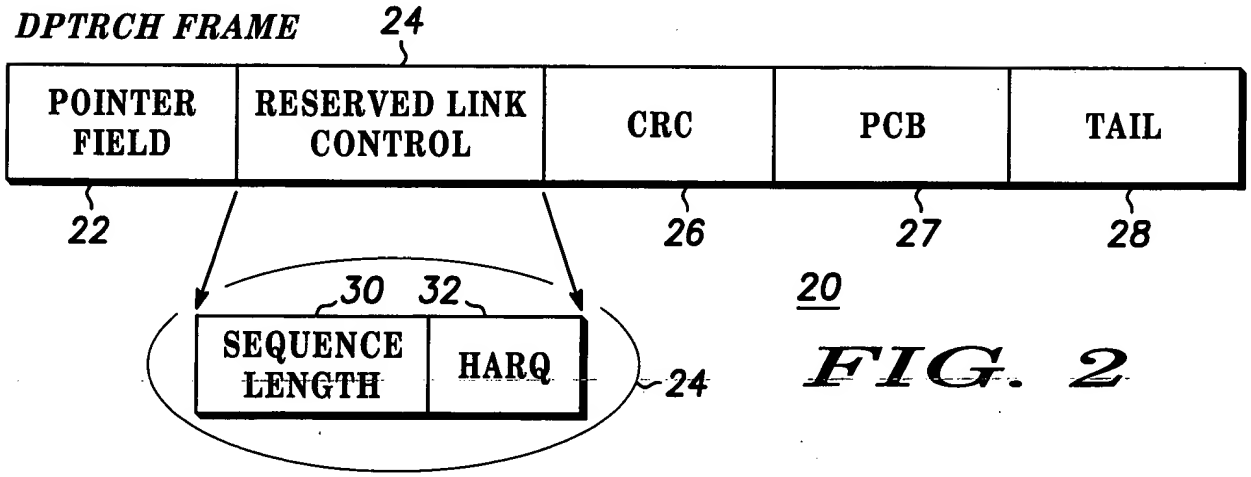

FIG. 2

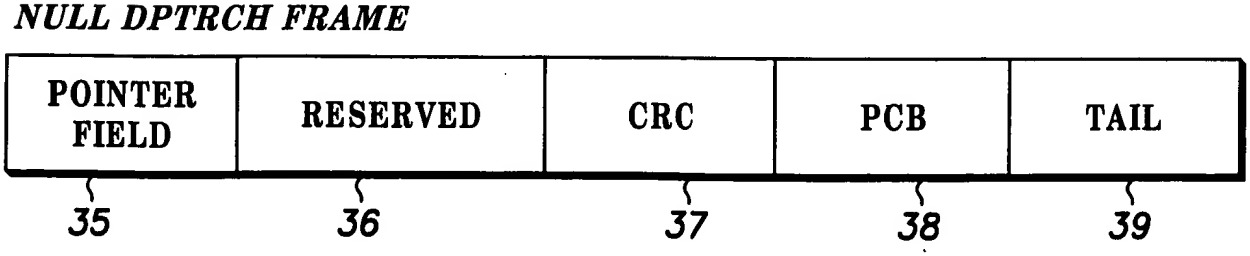

FIG. 3 34

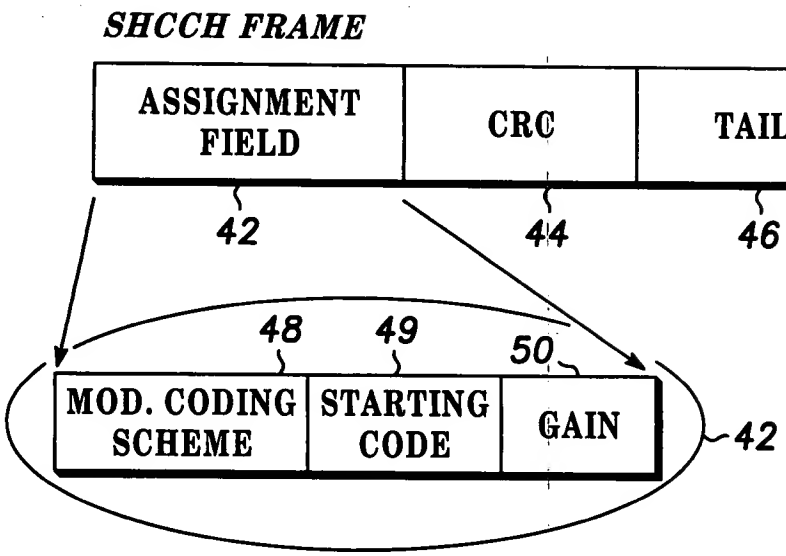
SHCCH FRAME
ASSIGNMENT FIELD
CRC
TAIL
42
44
46
48
49
50
MOD. CODING SCHEME
STARTING CODE
GAIN
42
40

FIG. 4

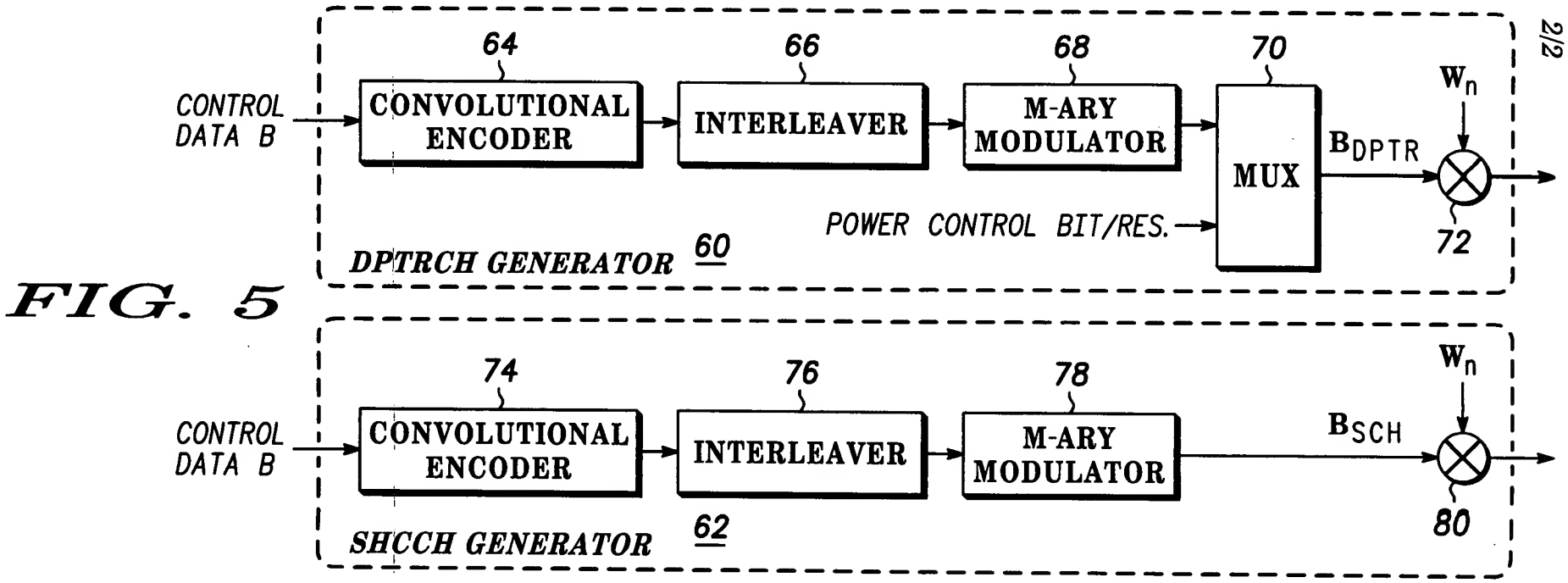
64
66
68
70
CONTROL DATA B
CONVOLUTIONAL ENCODER
INTERLEAVER
M-ARY MODULATOR
MUX
B_{DPTR}
W_n
POWER CONTROL BIT/RES.
72
DPTRCH GENERATOR 60
74
76
78
CONTROL DATA B
CONVOLUTIONAL ENCODER
INTERLEAVER
M-ARY MODULATOR
B_{SCH}
W_n
80
SHCCH GENERATOR 62

FIG. 5